Fig.1A

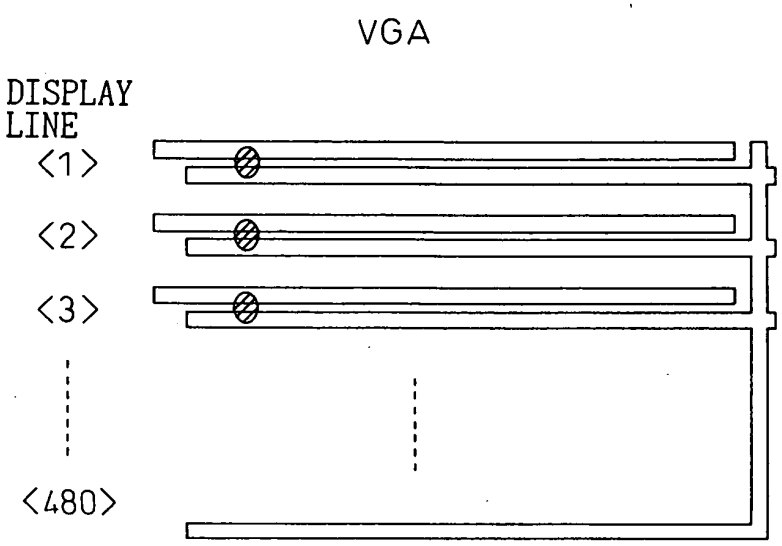
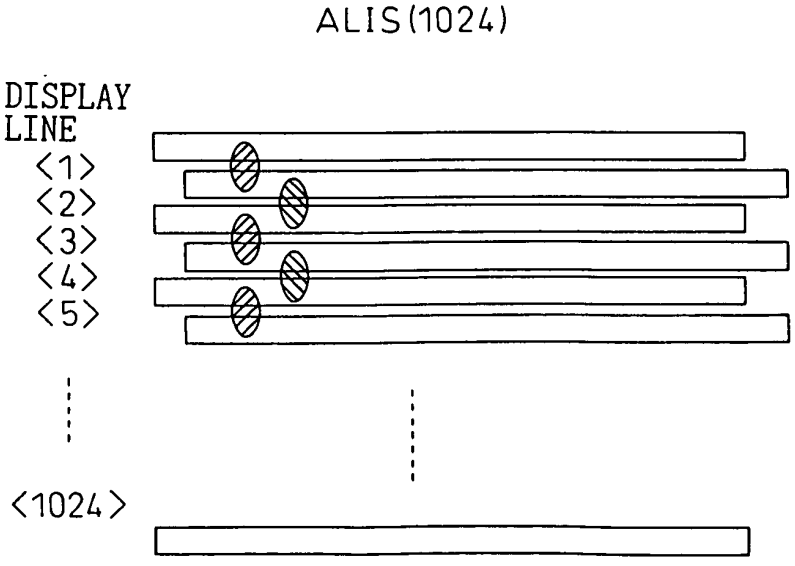



Fig.1B




0550691 - 4050P

Fig.2

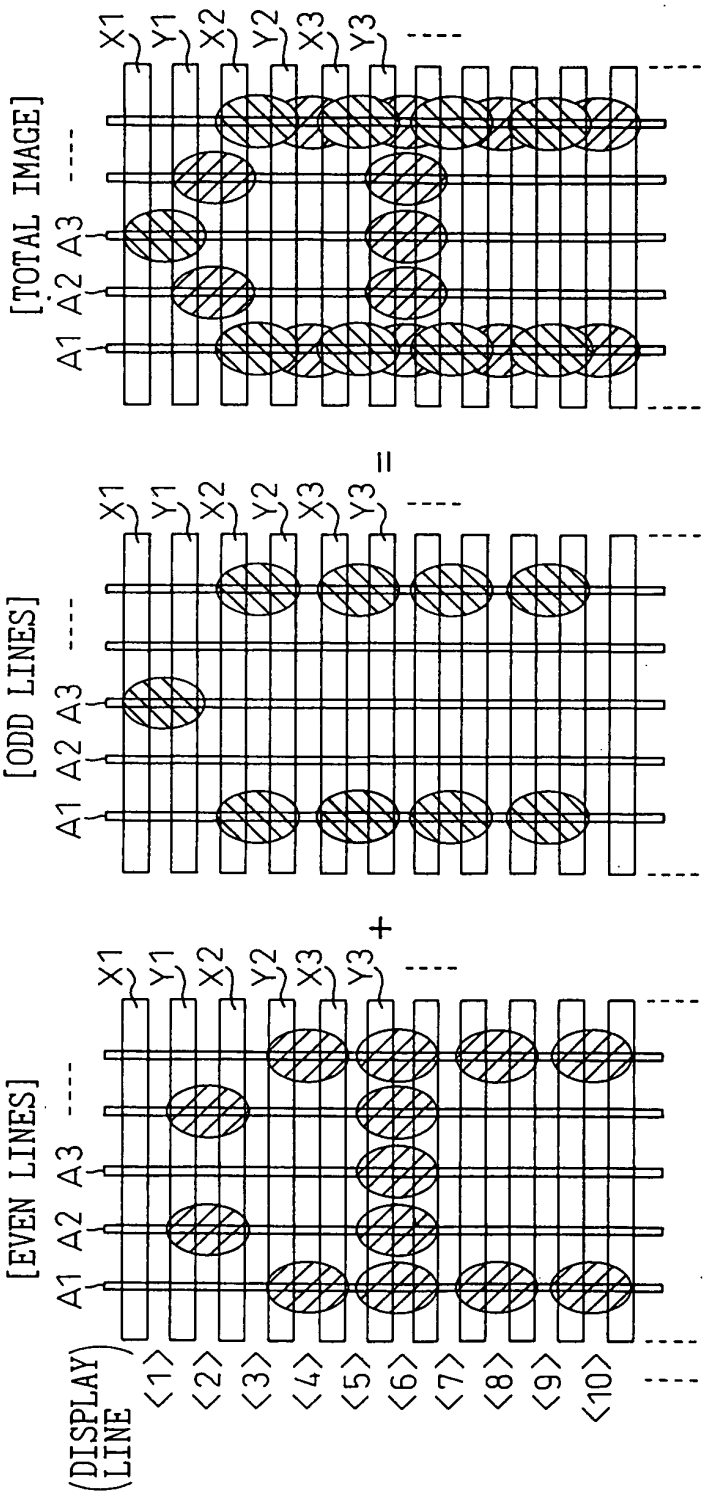

[TOTAL IMAGE]
[ODD LINES]
[EVEN LINES]
(DISPLAY LINE)
A1 A2 A3 ----
X1 Y1 X2 Y2 X3 Y3 ----
<1> <2> <3> <4> <5> <6> <7> <8> <9> <10> ----
=
+

Fig.3A

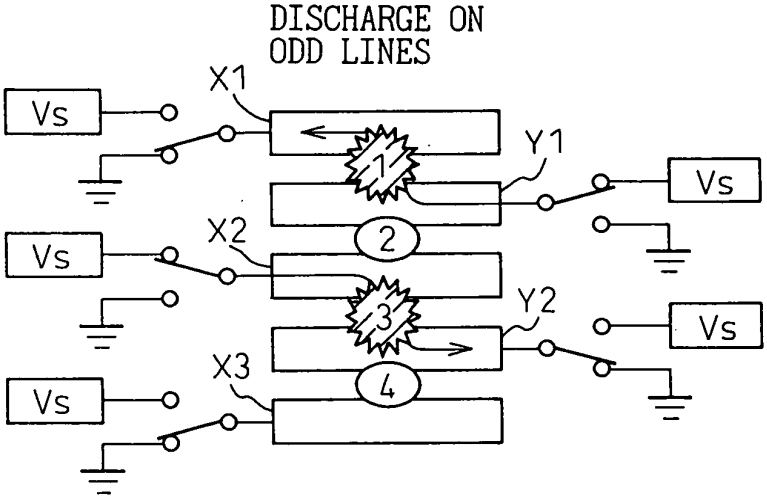

DISCHARGE ON
ODD LINES
X1
Y1
X2
Y2
X3
Vs
Vs
Vs
Vs
Vs
1
2
3
4

Fig.3B

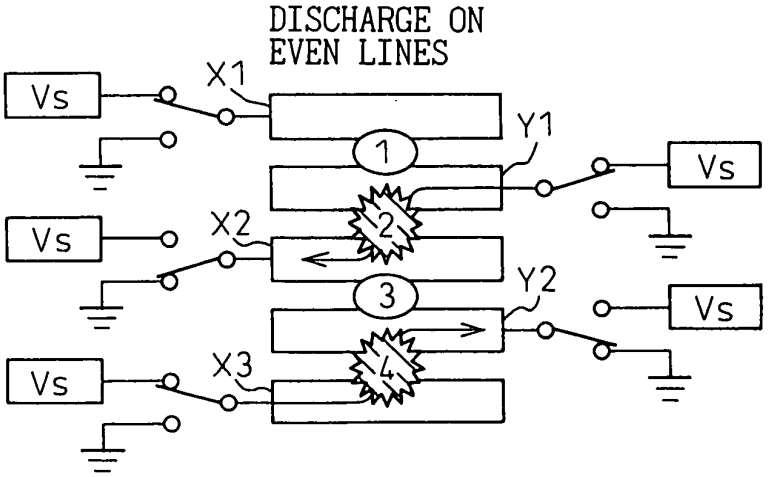

DISCHARGE ON
EVEN LINES
X1
Y1
X2
Y2
X3
Vs
Vs
Vs
Vs
Vs
1
2
3
4

Fig.4

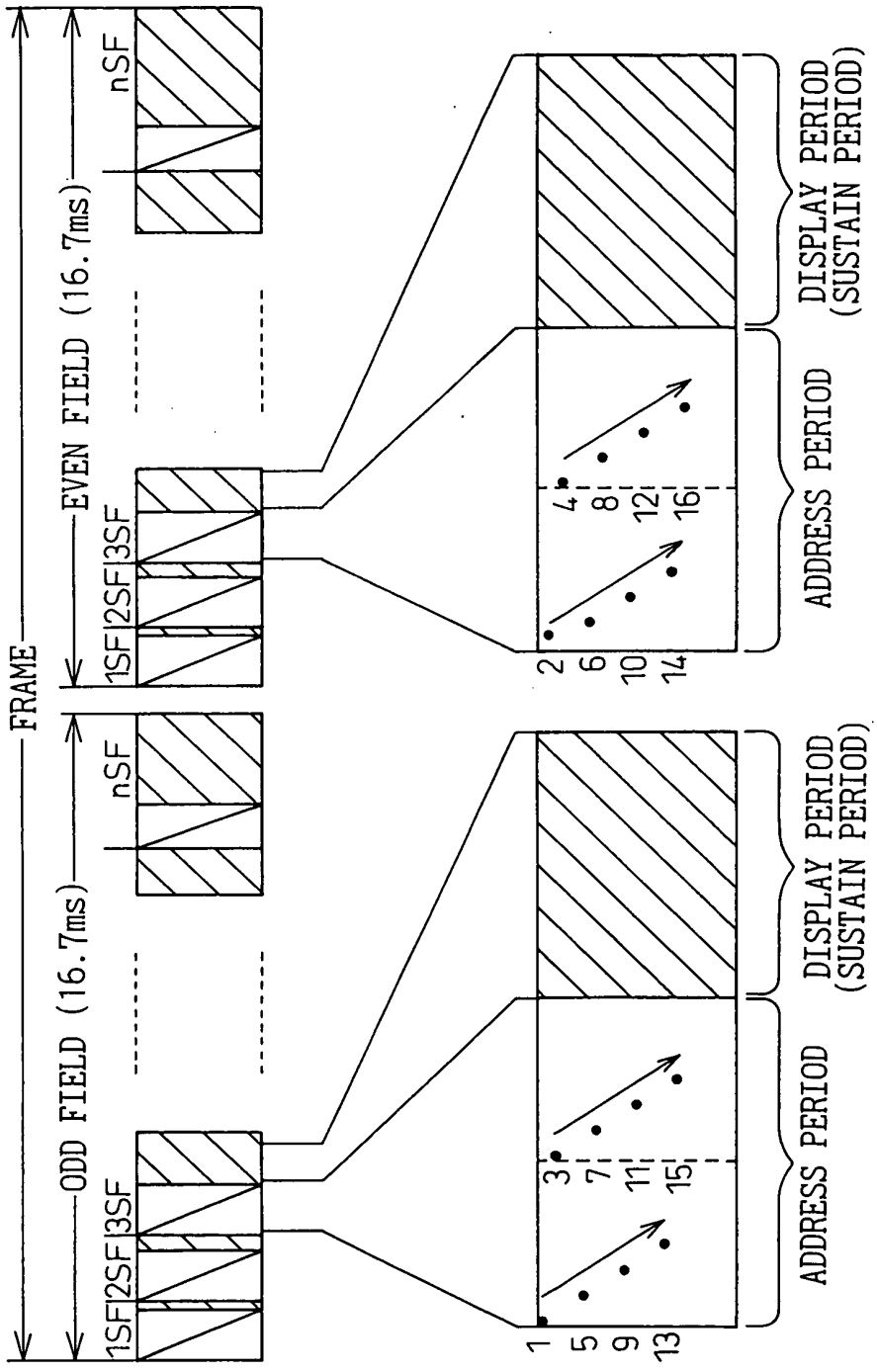

FRAME
ODD FIELD (16.7ms)
EVEN FIELD (16.7ms)
1SF
2SF
3SF
nSF
ADDRESS PERIOD
DISPLAY PERIOD
(SUSTAIN PERIOD)
1
5
9
13
2
6
10
14
3
7
11
15
4
8
12
16

Fig.5

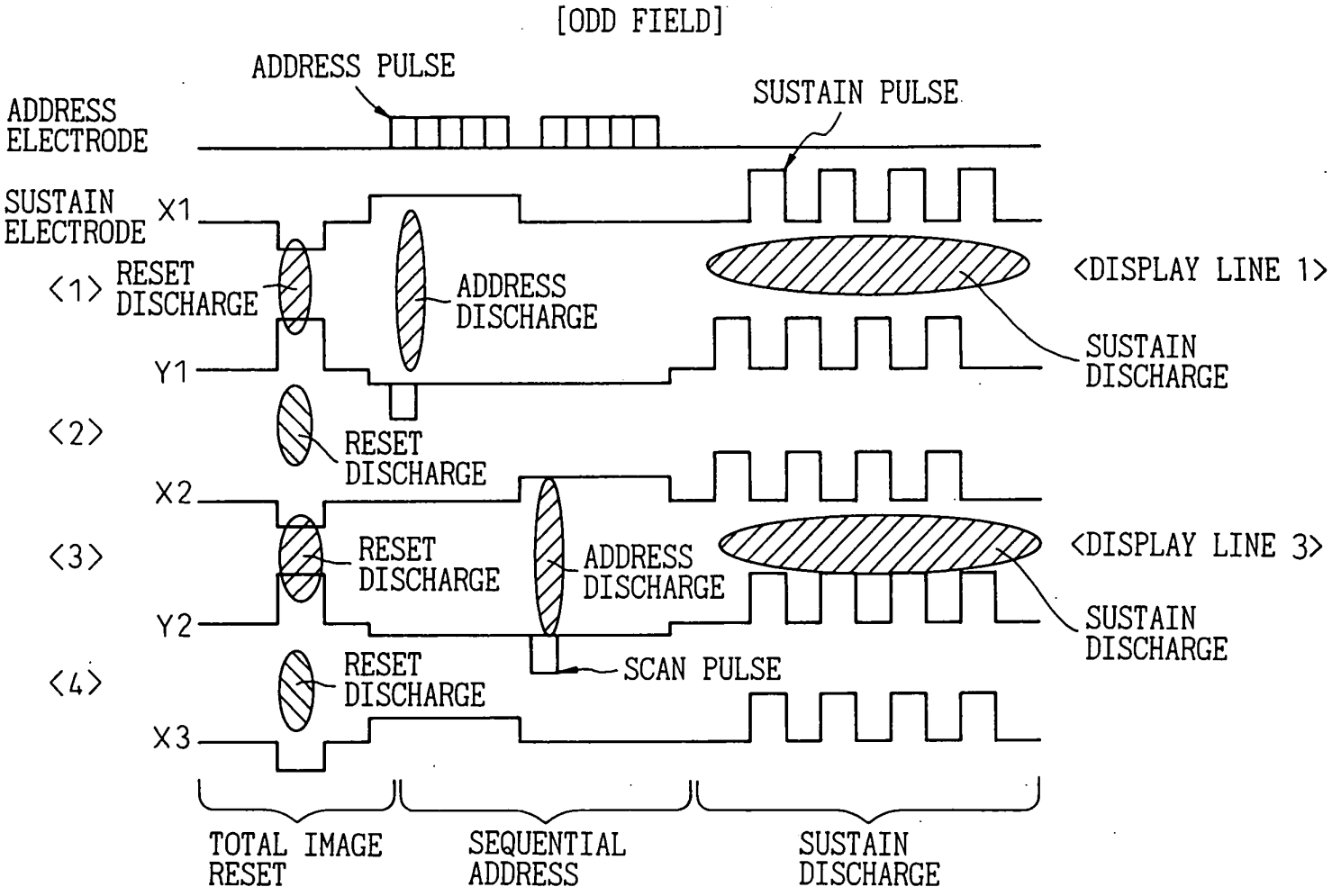

[ODD FIELD]
ADDRESS PULSE
SUSTAIN PULSE
ADDRESS ELECTRODE
SUSTAIN ELECTRODE
X1
<1>
RESET DISCHARGE
ADDRESS DISCHARGE
<DISPLAY LINE 1>
SUSTAIN DISCHARGE
Y1
<2>
RESET DISCHARGE
X2
<3>
RESET DISCHARGE
ADDRESS DISCHARGE
<DISPLAY LINE 3>
SUSTAIN DISCHARGE
Y2
<4>
RESET DISCHARGE
SCAN PULSE
X3
TOTAL IMAGE RESET
SEQUENTIAL ADDRESS
SUSTAIN DISCHARGE

Fig.6

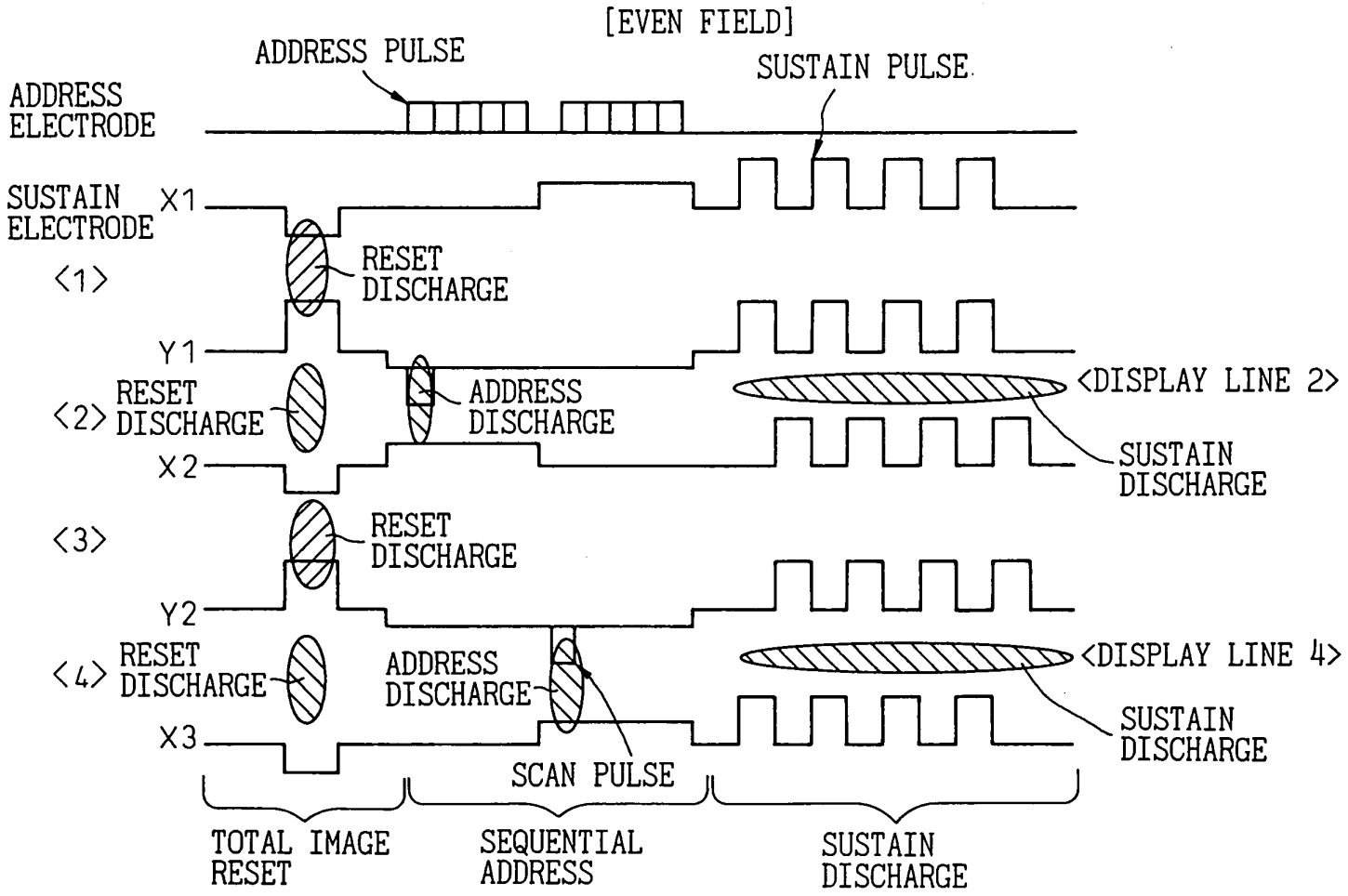

[EVEN FIELD]
ADDRESS PULSE
SUSTAIN PULSE
ADDRESS ELECTRODE
SUSTAIN ELECTRODE
X1
<1>
RESET DISCHARGE
Y1
<2>
RESET DISCHARGE
ADDRESS DISCHARGE
<DISPLAY LINE 2>
SUSTAIN DISCHARGE
X2
<3>
RESET DISCHARGE
Y2
<4>
RESET DISCHARGE
ADDRESS DISCHARGE
<DISPLAY LINE 4>
SUSTAIN DISCHARGE
X3
SCAN PULSE
TOTAL IMAGE RESET
SEQUENTIAL ADDRESS
SUSTAIN DISCHARGE

Fig.7

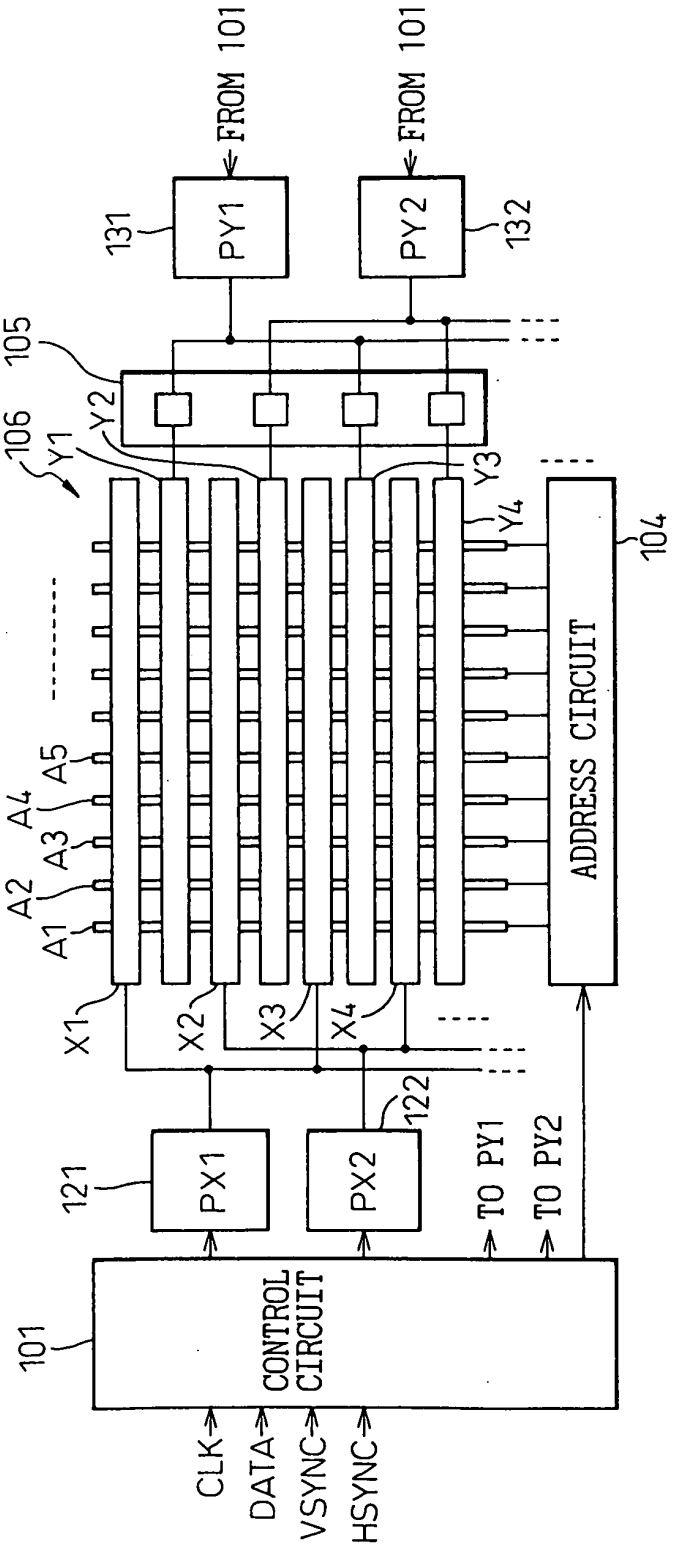

FROM 101
FROM 101
PY1
PY2
131
132
105
106
Y1
Y2
Y3
Y4
104
ADDRESS CIRCUIT
A1
A2
A3
A4
A5
X1
X2
X3
X4
122
121
PX1
PX2
TO PY1
TO PY2
101
CONTROL CIRCUIT
CLK
DATA
VSYNC
HSYNC

Fig.8

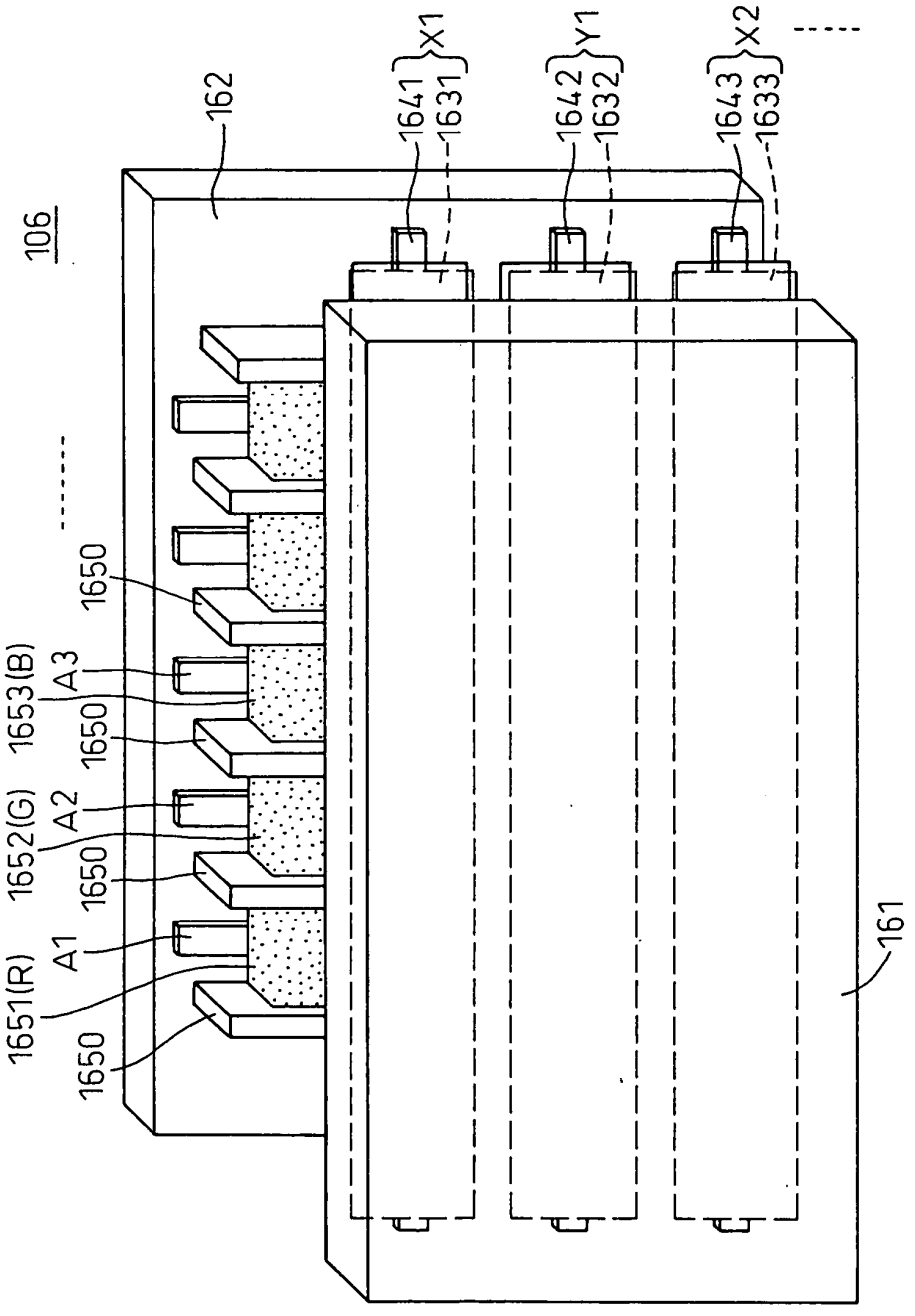

106
162
1641
1631
X1
1642
1632
Y1
1643
1633
X2
1650
1653(B)
A3
1650
1652(G)
A2
1650
1651(R)
A1
1650
161

Fig.9

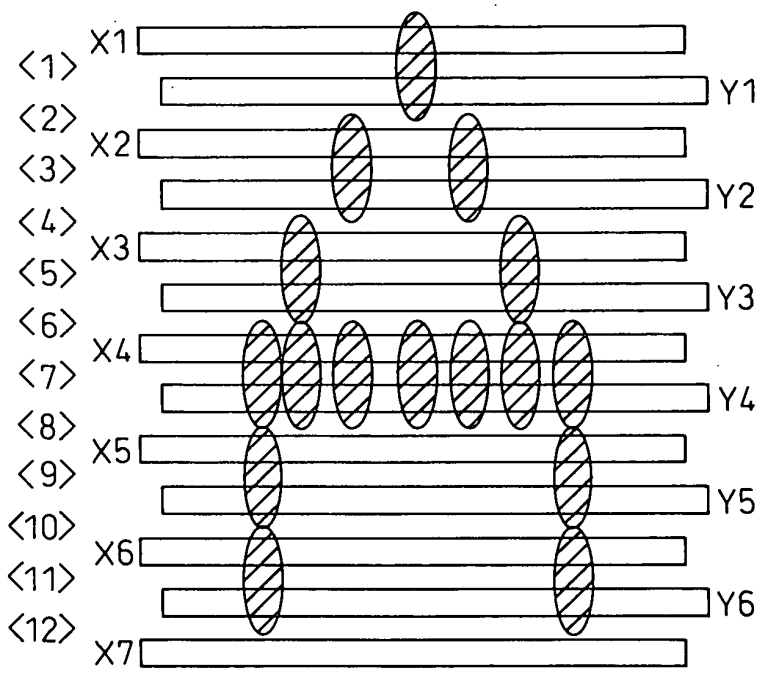


Fig.10

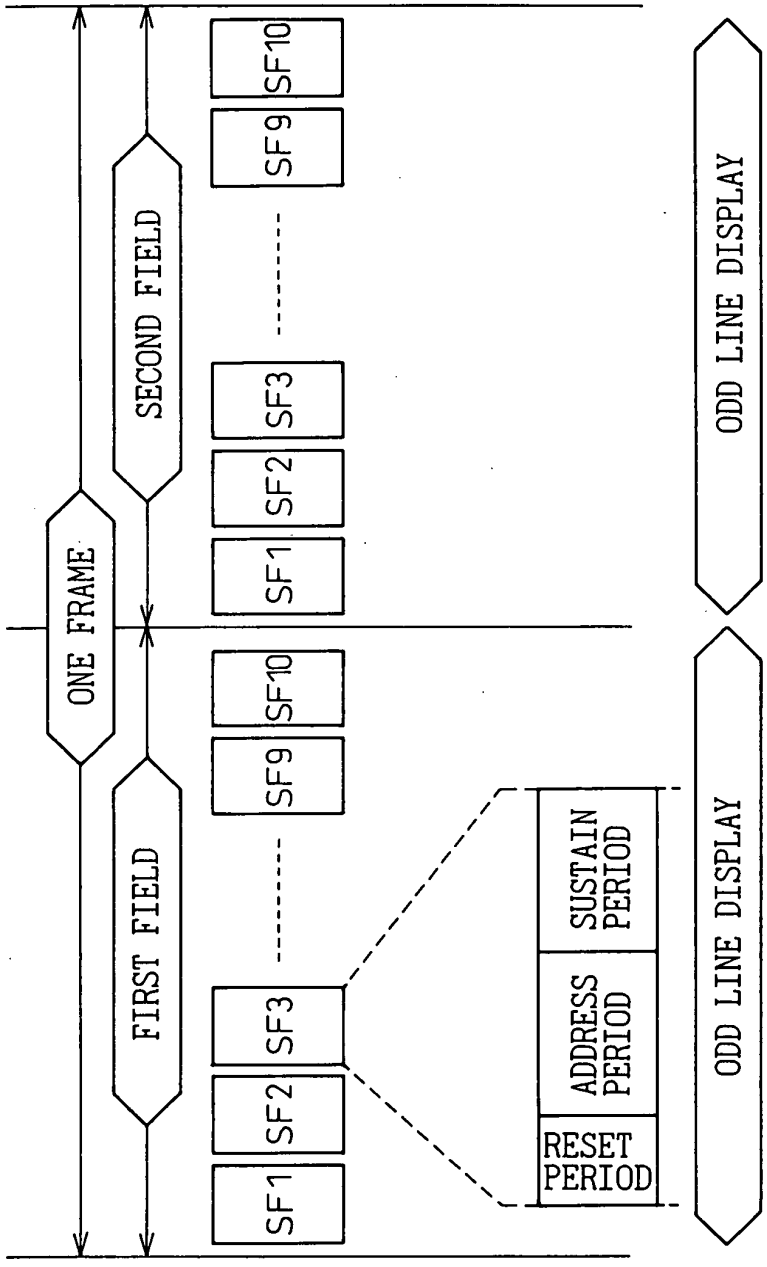
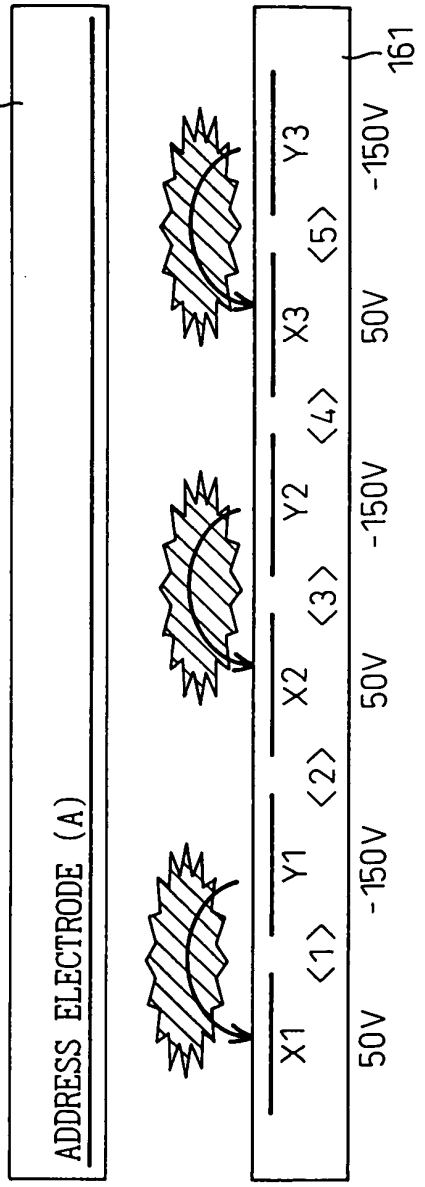

ONE FRAME
FIRST FIELD
SECOND FIELD
SF1
SF2
SF3
SF9
SF10
RESET PERIOD
ADDRESS PERIOD
SUSTAIN PERIOD
ODD LINE DISPLAY
ODD LINE DISPLAY

Fig.11


162
161
ADDRESS ELECTRODE (A)
X1
Y1
X2
Y2
X3
Y3
<1>
<2>
<3>
<4>
<5>
50V
-150V
50V
-150V
50V
-150V

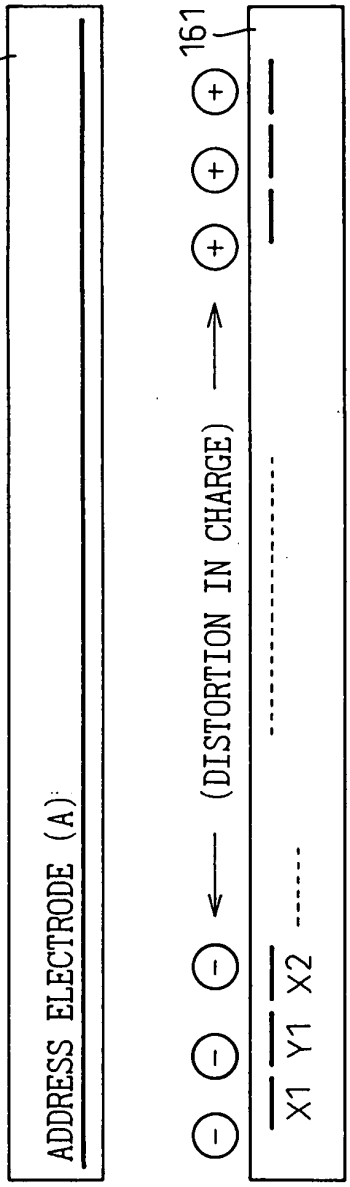



Fig.12A

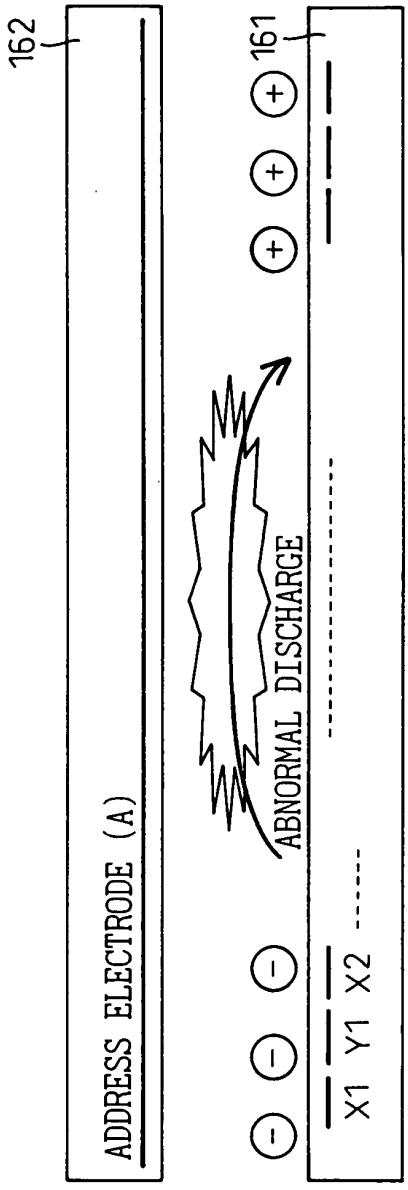



Fig.12B

Fig.13

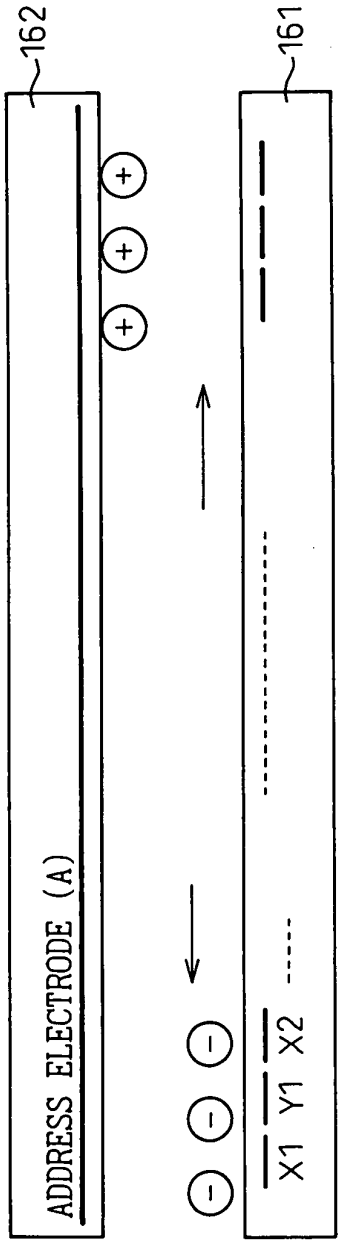
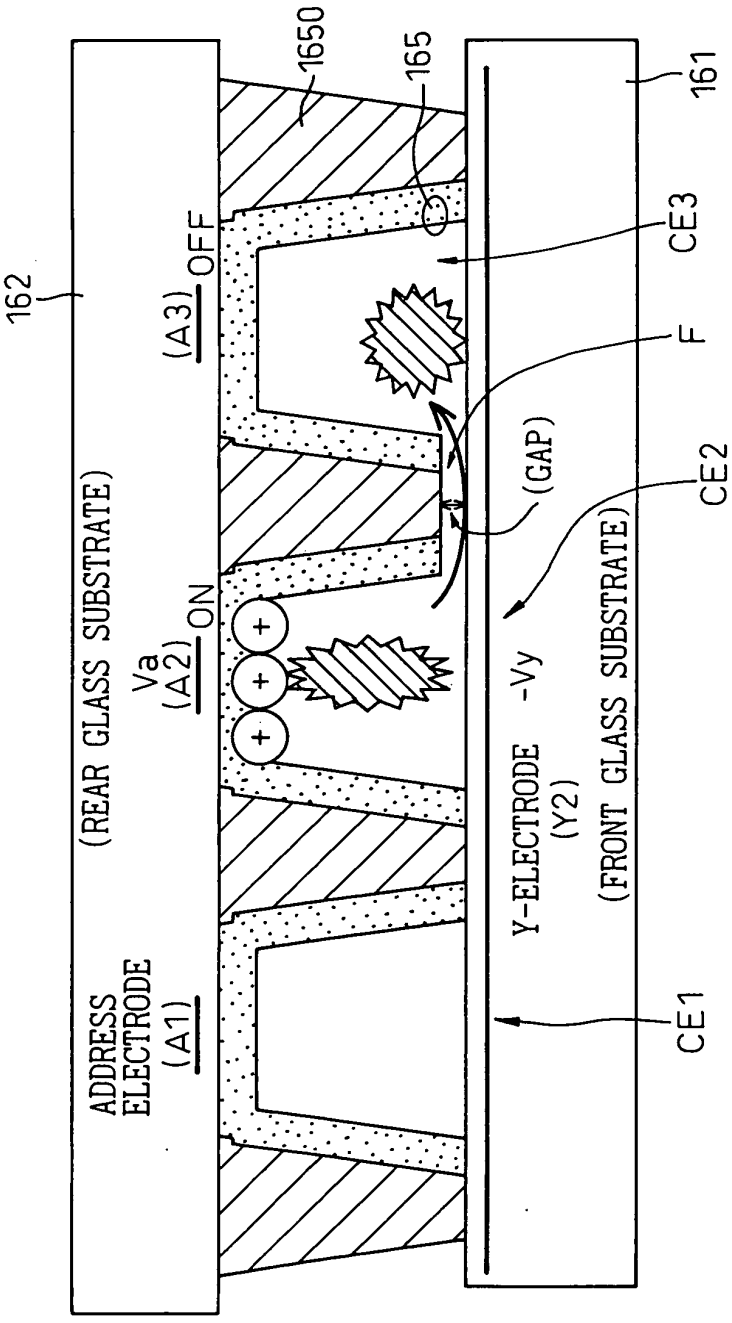


Fig.14


1650
165
161
162
(A3) OFF
CE3
F
(GAP)
CE2
Va
(A2) ON
-Vy
ADDRESS
ELECTRODE
(A1)
(REAR GLASS SUBSTRATE)
Y-ELECTRODE
(Y2)
(FRONT GLASS SUBSTRATE)
CE1

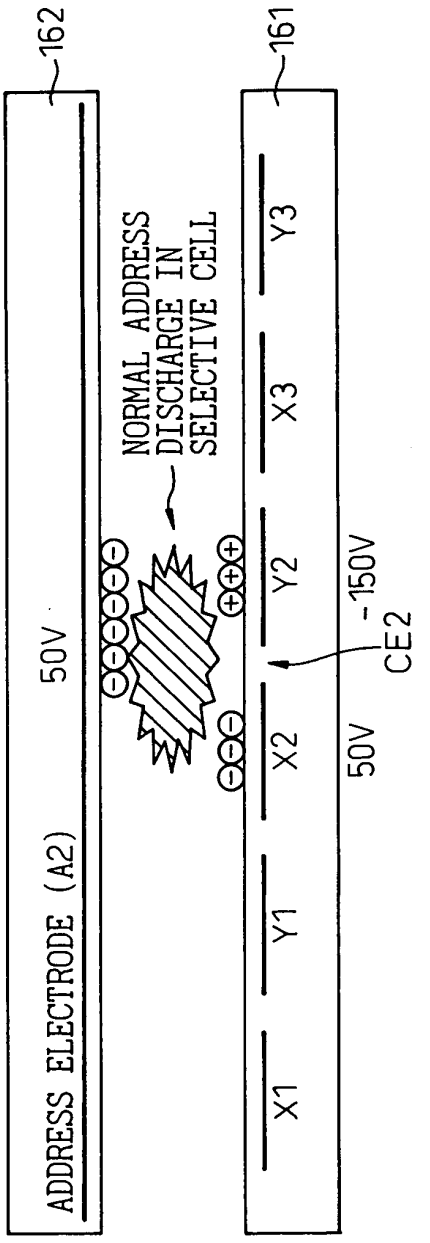



Fig.15A

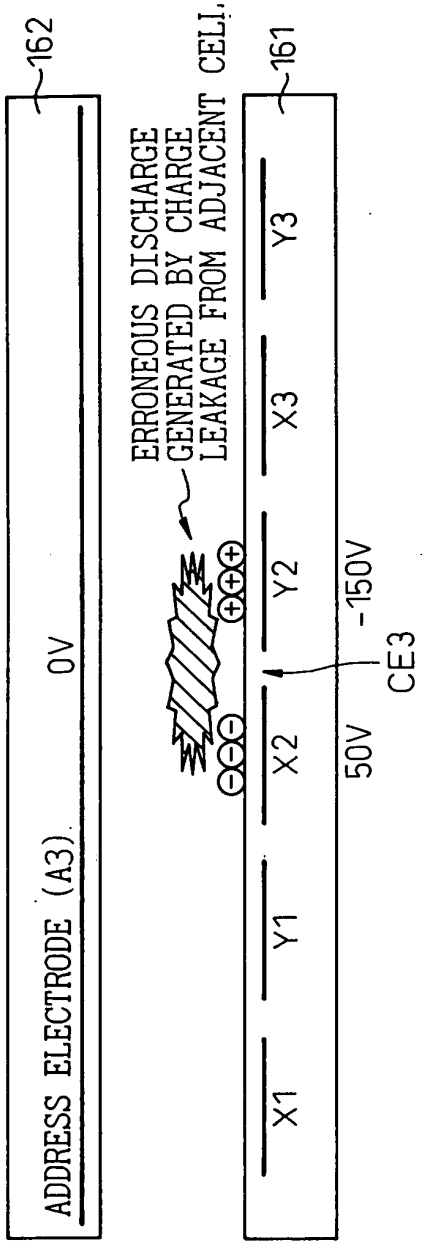



Fig.15B

Fig.16

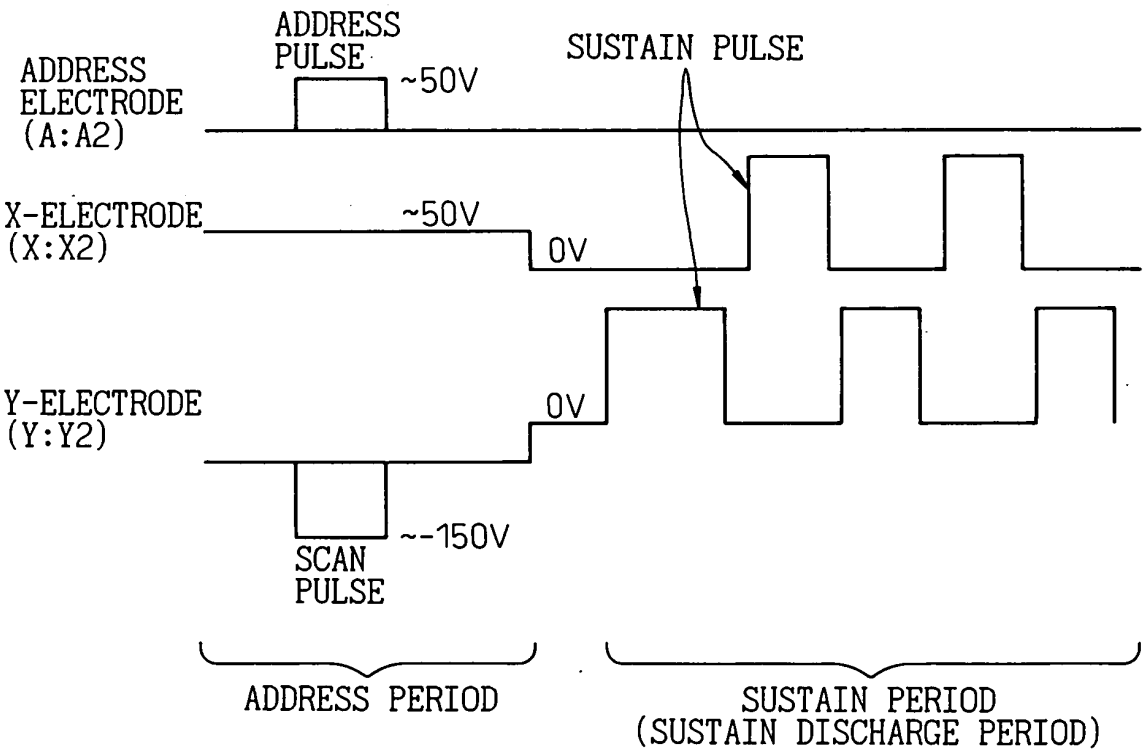

ADDRESS PULSE
SUSTAIN PULSE
ADDRESS ELECTRODE (A:A2)
~50V
X-ELECTRODE (X:X2)
~50V
0V
Y-ELECTRODE (Y:Y2)
0V
--150V
SCAN PULSE
ADDRESS PERIOD
SUSTAIN PERIOD (SUSTAIN DISCHARGE PERIOD)

Fig.17

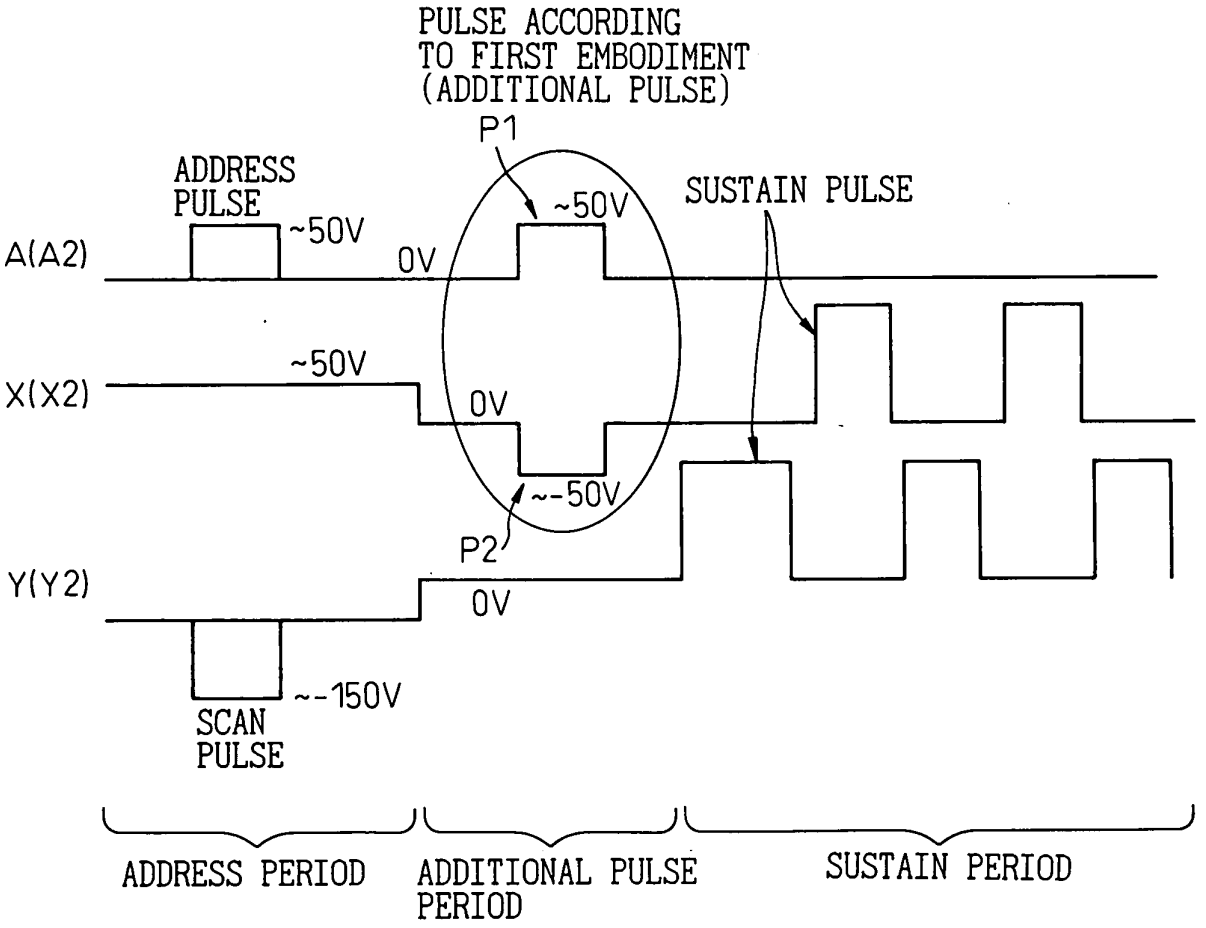

PULSE ACCORDING
TO FIRST EMBODIMENT
(ADDITIONAL PULSE)
P1
ADDRESS
PULSE
~50V
SUSTAIN PULSE
A(A2)
0V
~50V
~50V
X(X2)
0V
~-50V
P2
Y(Y2)
0V
~-150V
SCAN
PULSE
ADDRESS PERIOD
ADDITIONAL PULSE
PERIOD
SUSTAIN PERIOD

Fig.18A

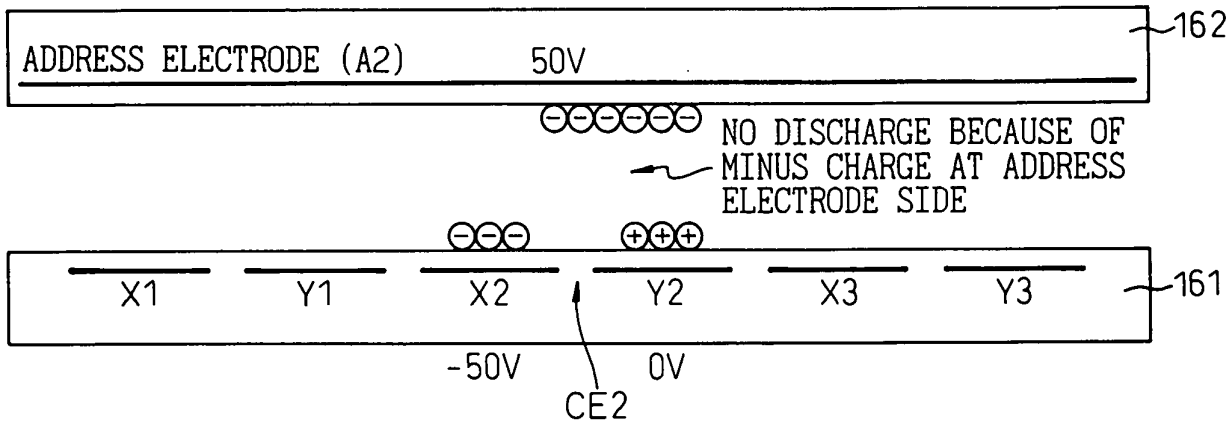



Fig.18B

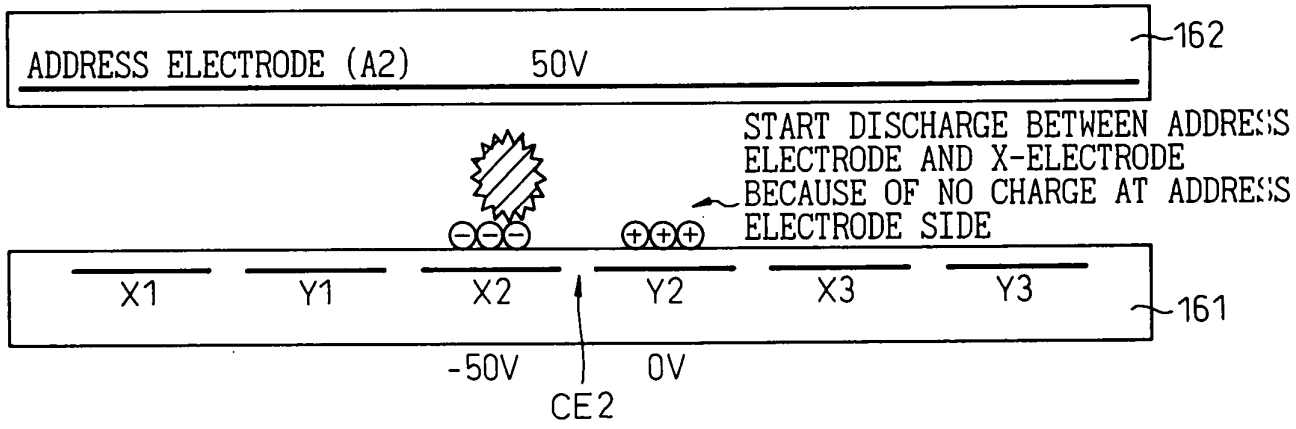


Fig.19

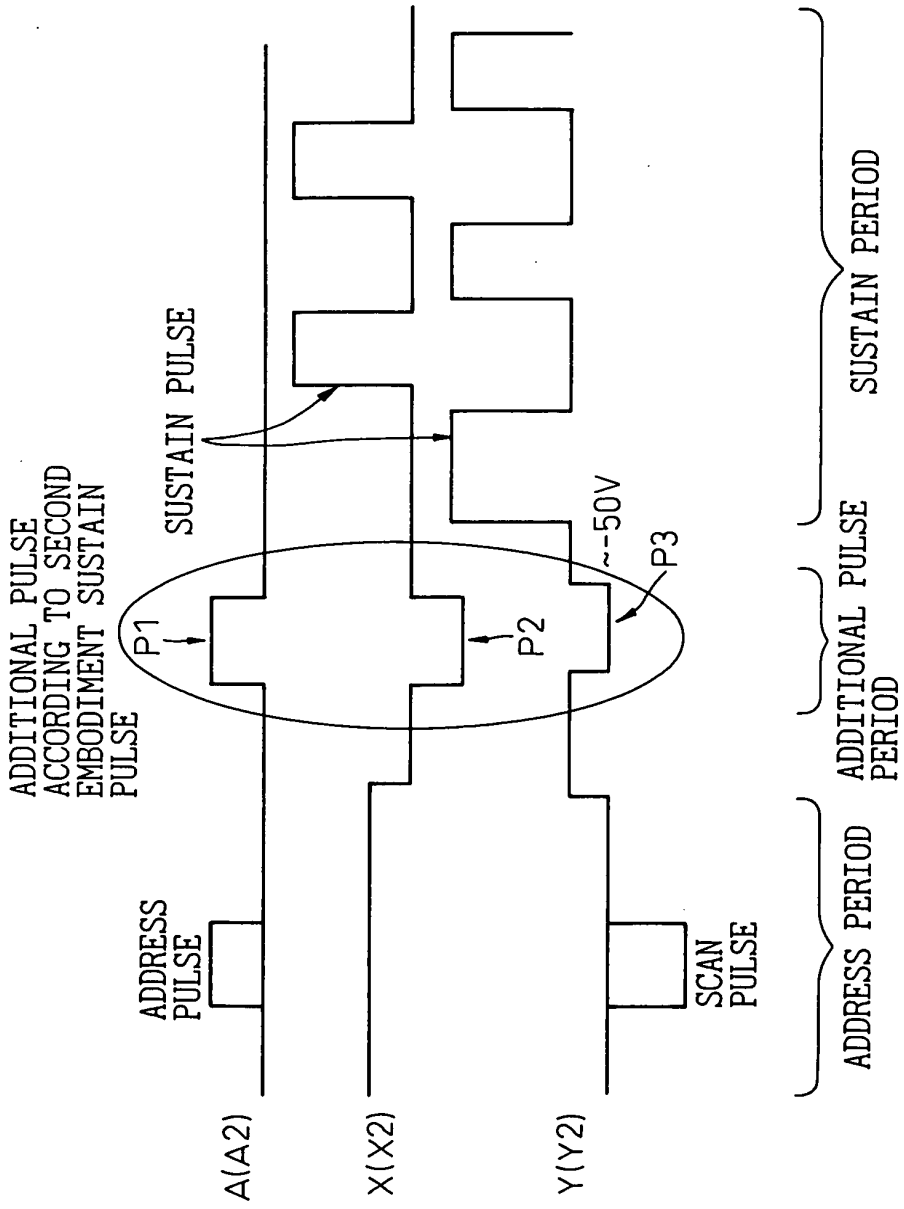

ADDITIONAL PULSE ACCORDING TO SECOND EMBODIMENT SUSTAIN PULSE
SUSTAIN PULSE
P1
P2
P3
~-50V
ADDRESS PULSE
SCAN PULSE
A(A2)
X(X2)
Y(Y2)
ADDRESS PERIOD
ADDITIONAL PULSE PERIOD
SUSTAIN PERIOD

Fig.20

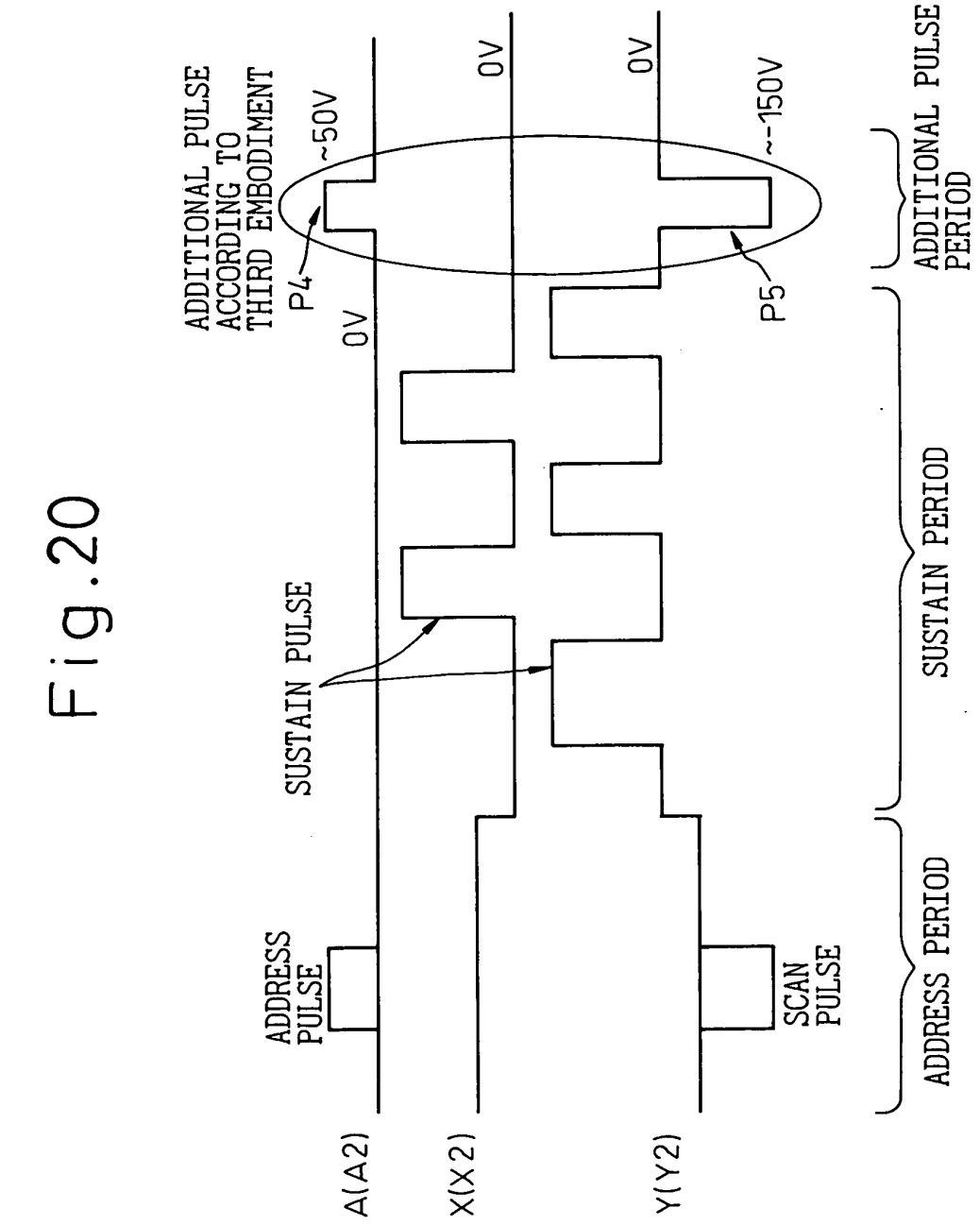

ADDITIONAL PULSE ACCORDING TO THIRD EMBODIMENT
P4
~50V
0V
0V
0V
~-150V
P5
SUSTAIN PULSE
ADDRESS PULSE
SCAN PULSE
A(A2)
X(X2)
Y(Y2)
ADDRESS PERIOD
SUSTAIN PERIOD
ADDITIONAL PULSE PERIOD

Fig.21

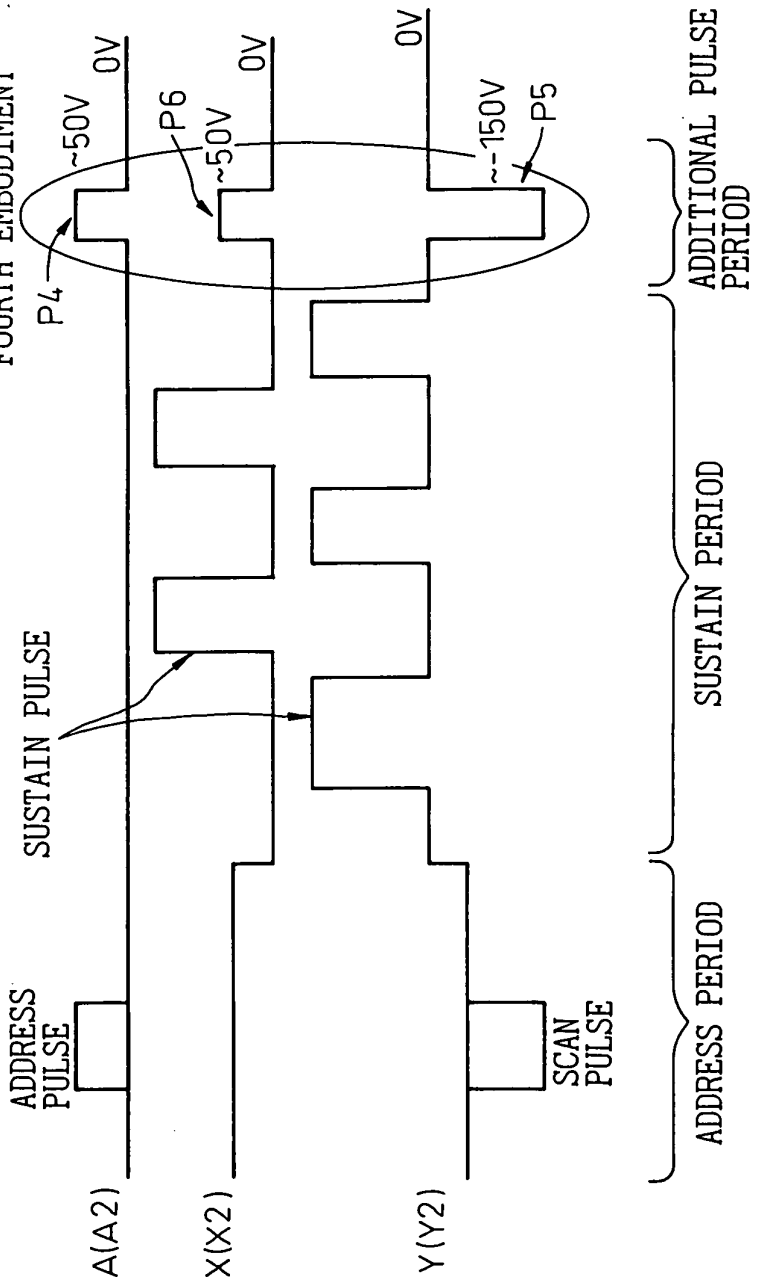

ADDITIONAL PULSE ACCORDING TO FOURTH EMBODIMENT
P4
P6
P5
~50V
~50V
~-150V
0V
0V
0V
SUSTAIN PULSE
ADDRESS PULSE
SCAN PULSE
A(A2)
X(X2)
Y(Y2)
ADDRESS PERIOD
SUSTAIN PERIOD
ADDITIONAL PULSE PERIOD

Fig.22

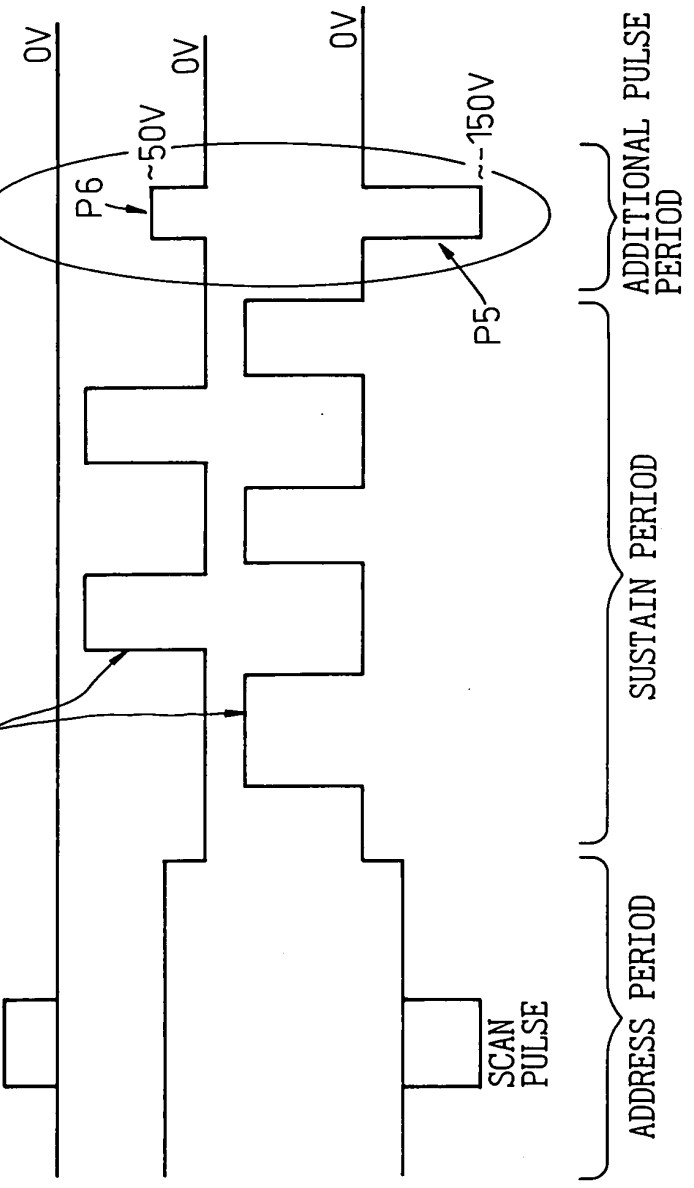

ADDITIONAL PULSE ACCORDING TO FIFTH EMBODIMENT
SUSTAIN PULSE
ADDRESS PULSE
SCAN PULSE
0V
0V
0V
~50V
~-150V
P6
P5
A(A2)
X(X2)
Y(Y2)
ADDRESS PERIOD
SUSTAIN PERIOD
ADDITIONAL PULSE PERIOD

Fig.23

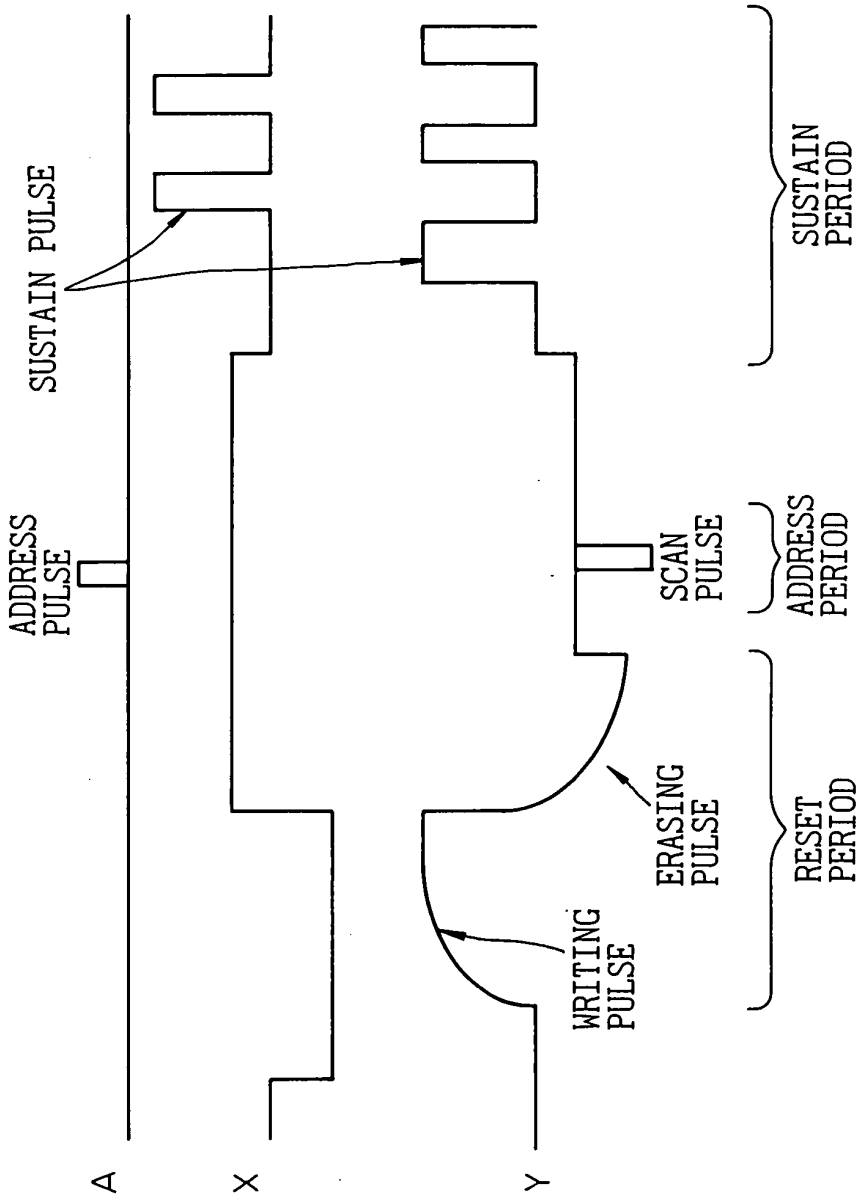

SUSTAIN PULSE
ADDRESS PULSE
SCAN PULSE
ERASING PULSE
WRITING PULSE
A
X
Y
SUSTAIN PERIOD
ADDRESS PERIOD
RESET PERIOD

Fig.24

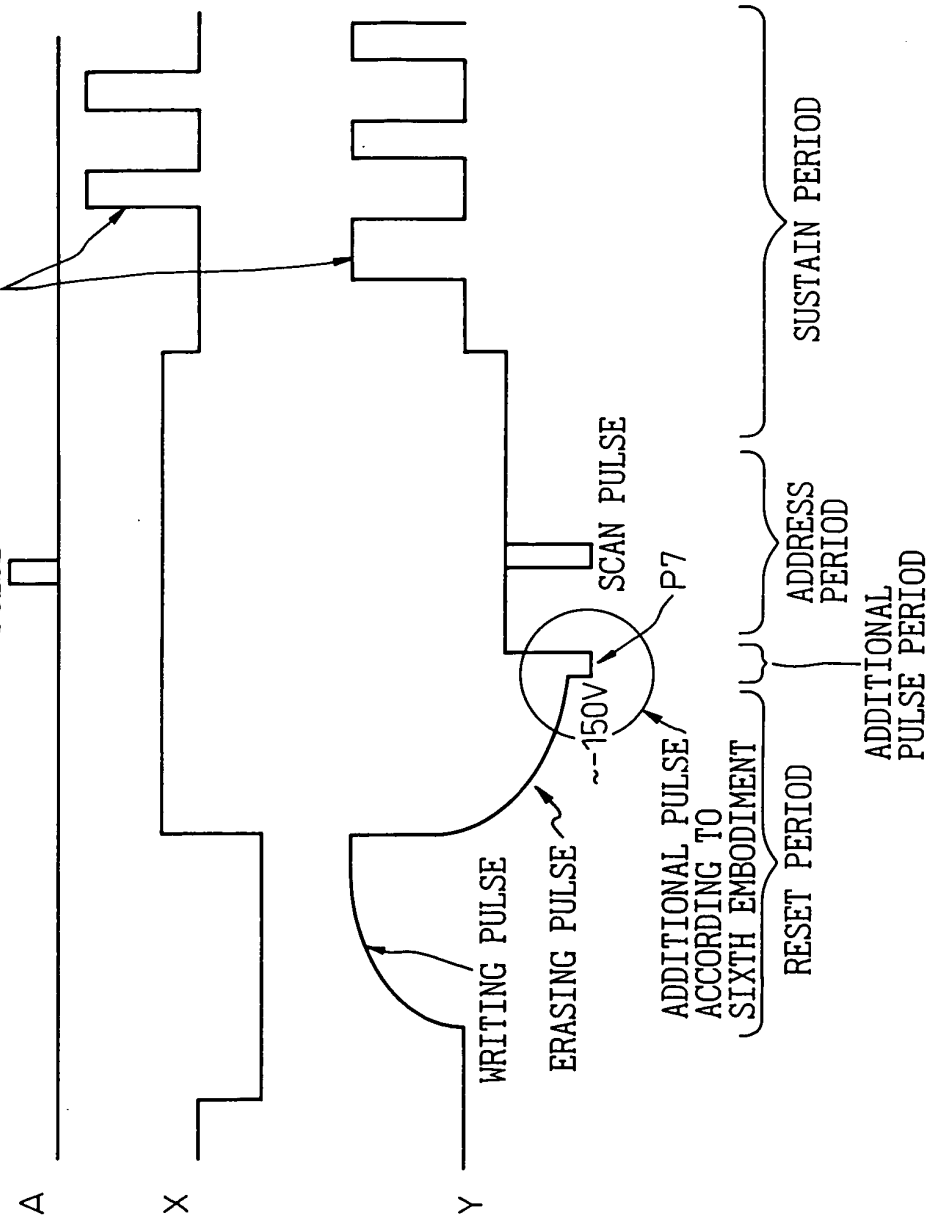

SUSTAIN PULSE
ADDRESS PULSE
SCAN PULSE
P7
~-150V
WRITING PULSE
ERASING PULSE
ADDITIONAL PULSE ACCORDING TO SIXTH EMBODIMENT
RESET PERIOD
ADDRESS PERIOD
ADDITIONAL PULSE PERIOD
SUSTAIN PERIOD
A
X
Y

Fig. 25

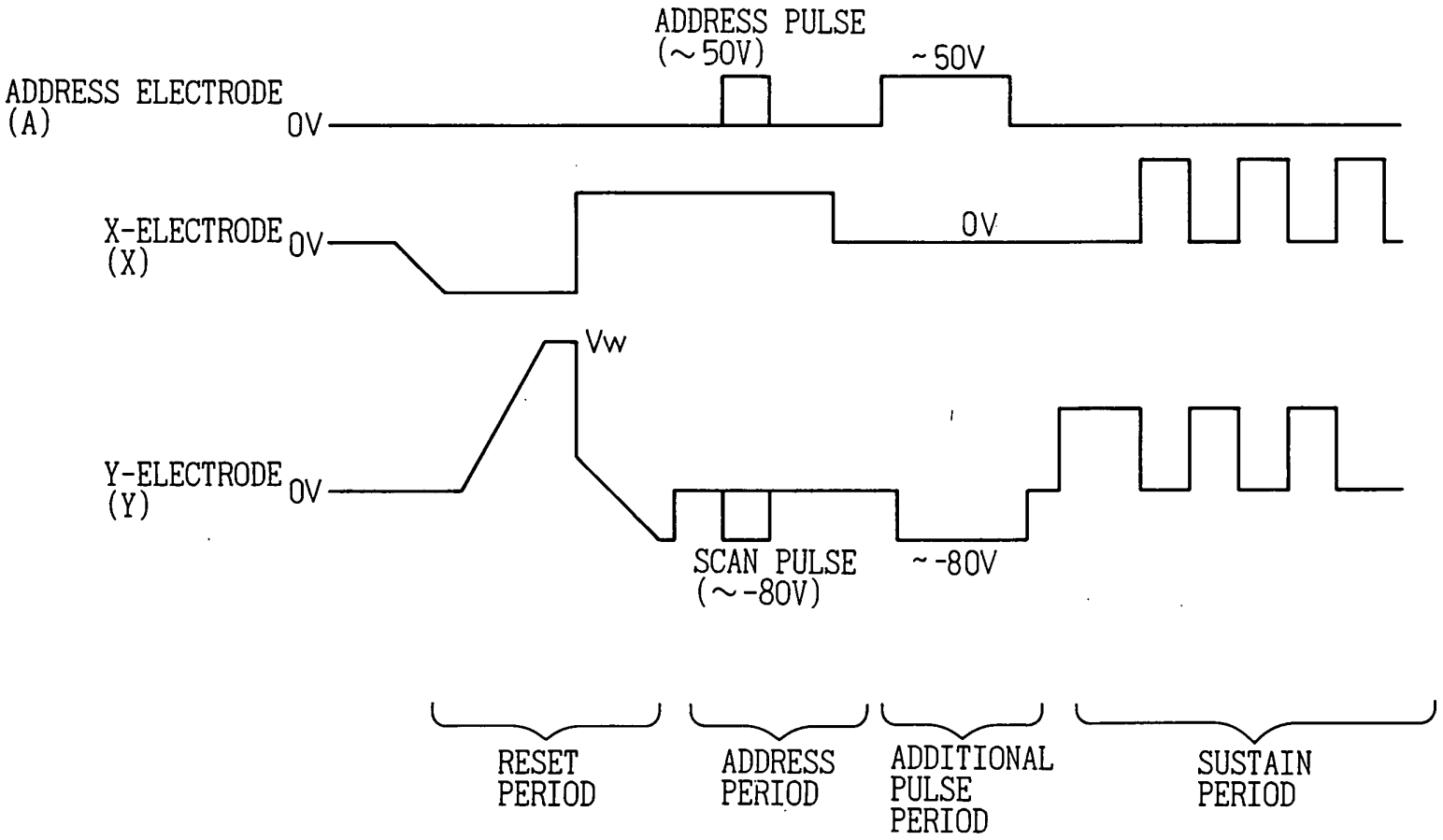

ADDRESS PULSE
(~50V)
~50V
ADDRESS ELECTRODE
(A)
0V
X-ELECTRODE
(X)
0V
0V
Vw
Y-ELECTRODE
(Y)
0V
SCAN PULSE
(~-80V)
~-80V
RESET
PERIOD
ADDRESS
PERIOD
ADDITIONAL
PULSE
PERIOD
SUSTAIN
PERIOD

Fig.26A

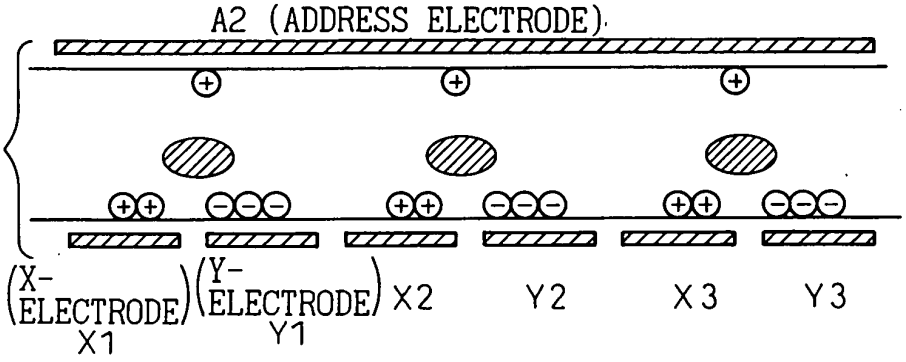



Fig.26B

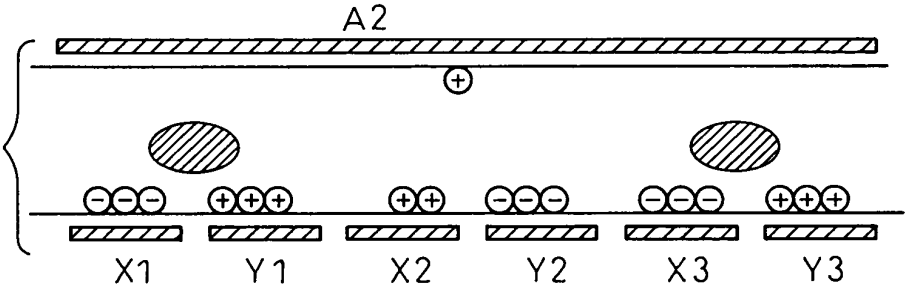



Fig.26C

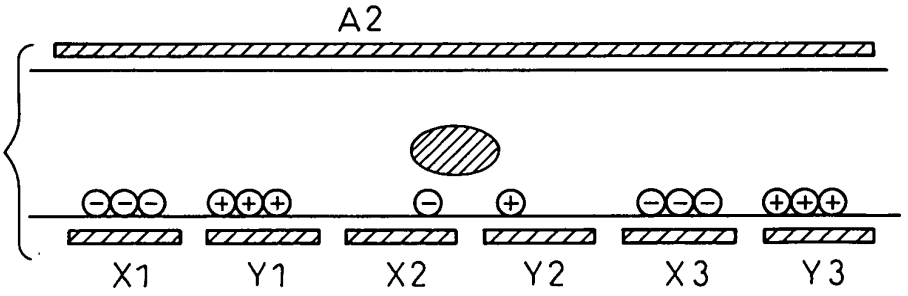



Fig.26D

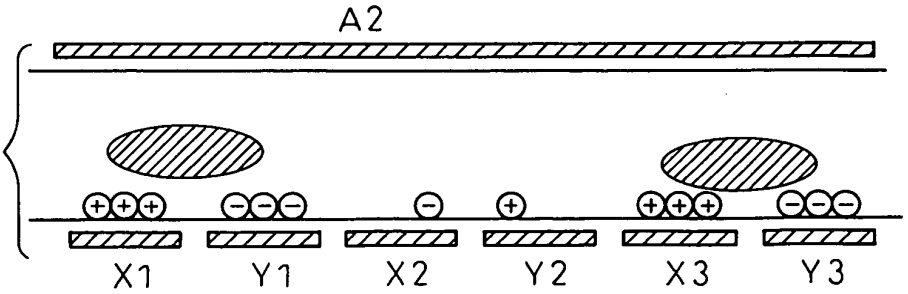


Fig.27

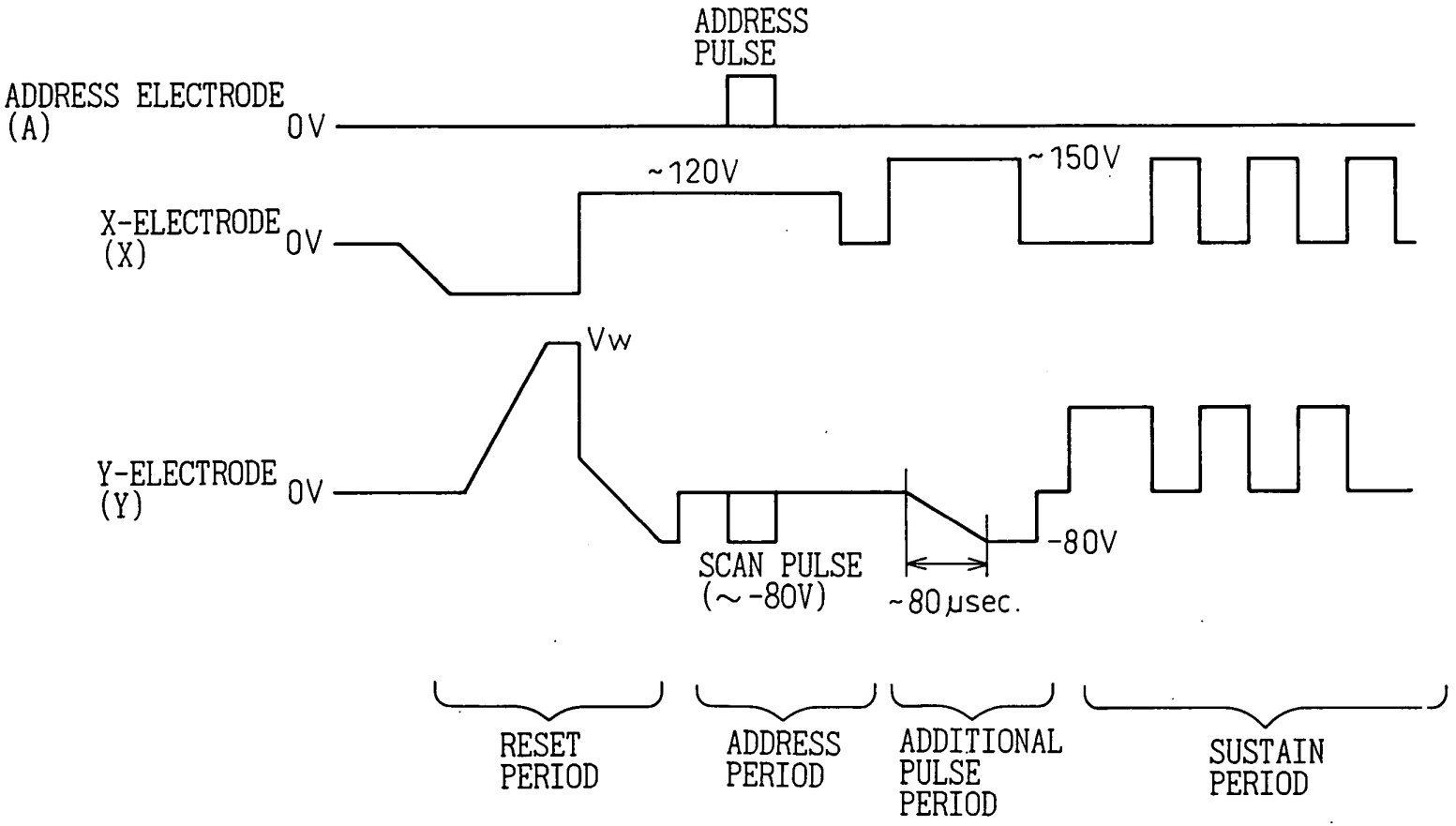

ADDRESS PULSE
ADDRESS ELECTRODE (A)
0V
~120V
~150V
X-ELECTRODE (X)
0V
Vw
Y-ELECTRODE (Y)
0V
SCAN PULSE (~-80V)
~80μsec.
-80V
RESET PERIOD
ADDRESS PERIOD
ADDITIONAL PULSE PERIOD
SUSTAIN PERIOD

Fig.28

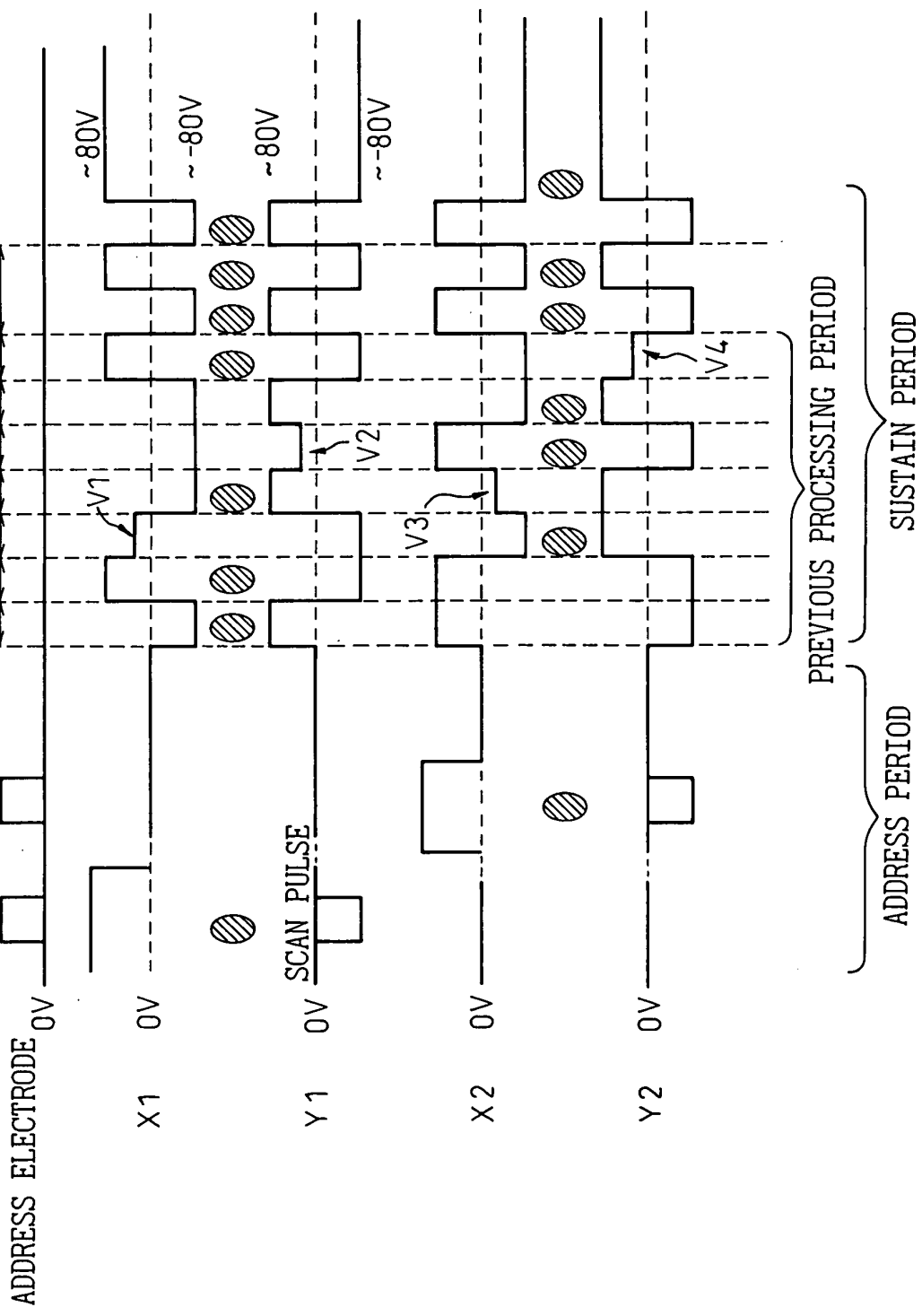

ADDRESS PULSE
SCAN PULSE
ADDRESS ELECTRODE
X1
Y1
X2
Y2
0V
~80V
~-80V
T1 T2 T3 T4 T5 T6 T7 T8
V1
V2
V3
V4
PREVIOUS PROCESSING PERIOD
SUSTAIN PERIOD
ADDRESS PERIOD

Fig.29

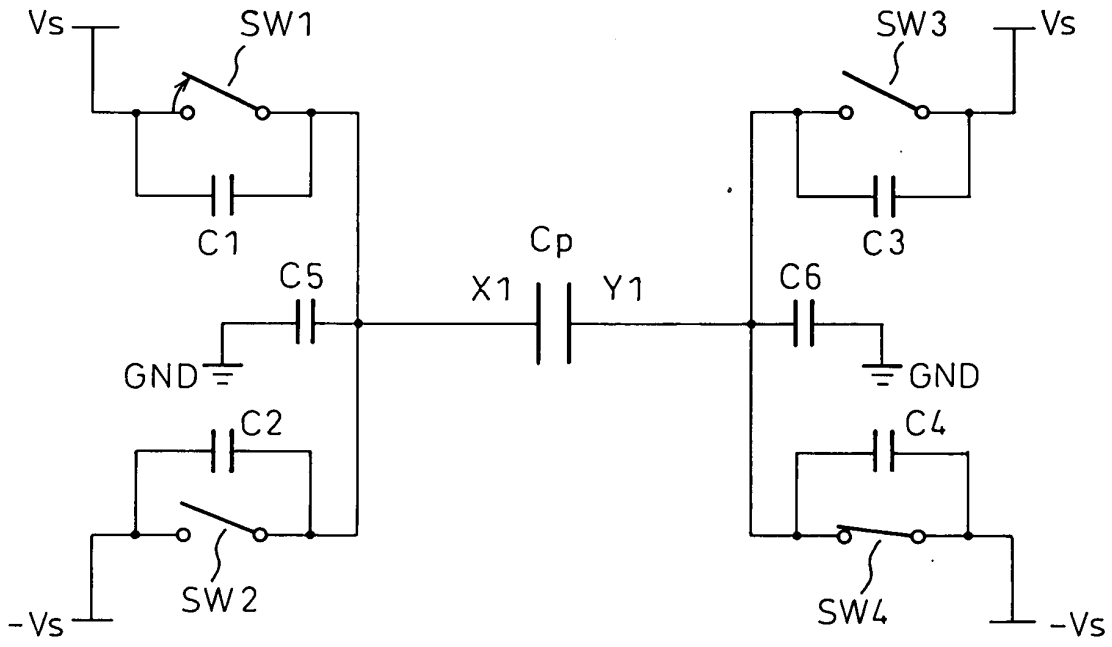
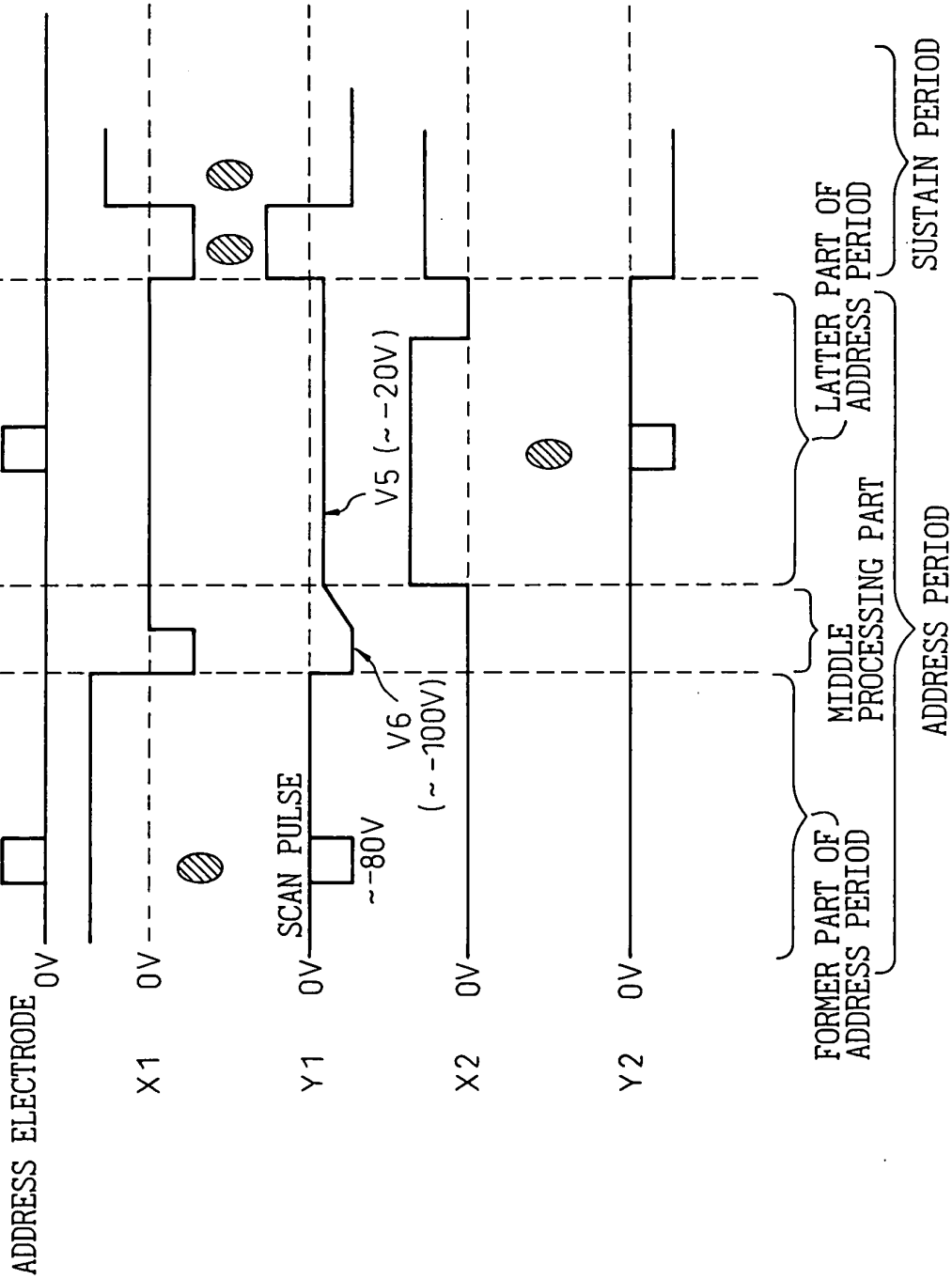


Fig.30


ADDRESS ELECTRODE
ADDRESS PULSE
0V
X1
0V
Y1
SCAN PULSE
0V
~-80V
V6
(~-100V)
V5 (~-20V)
X2
0V
Y2
0V
FORMER PART OF ADDRESS PERIOD
MIDDLE PROCESSING PART
LATTER PART OF ADDRESS PERIOD
ADDRESS PERIOD
SUSTAIN PERIOD